

FIG. 1

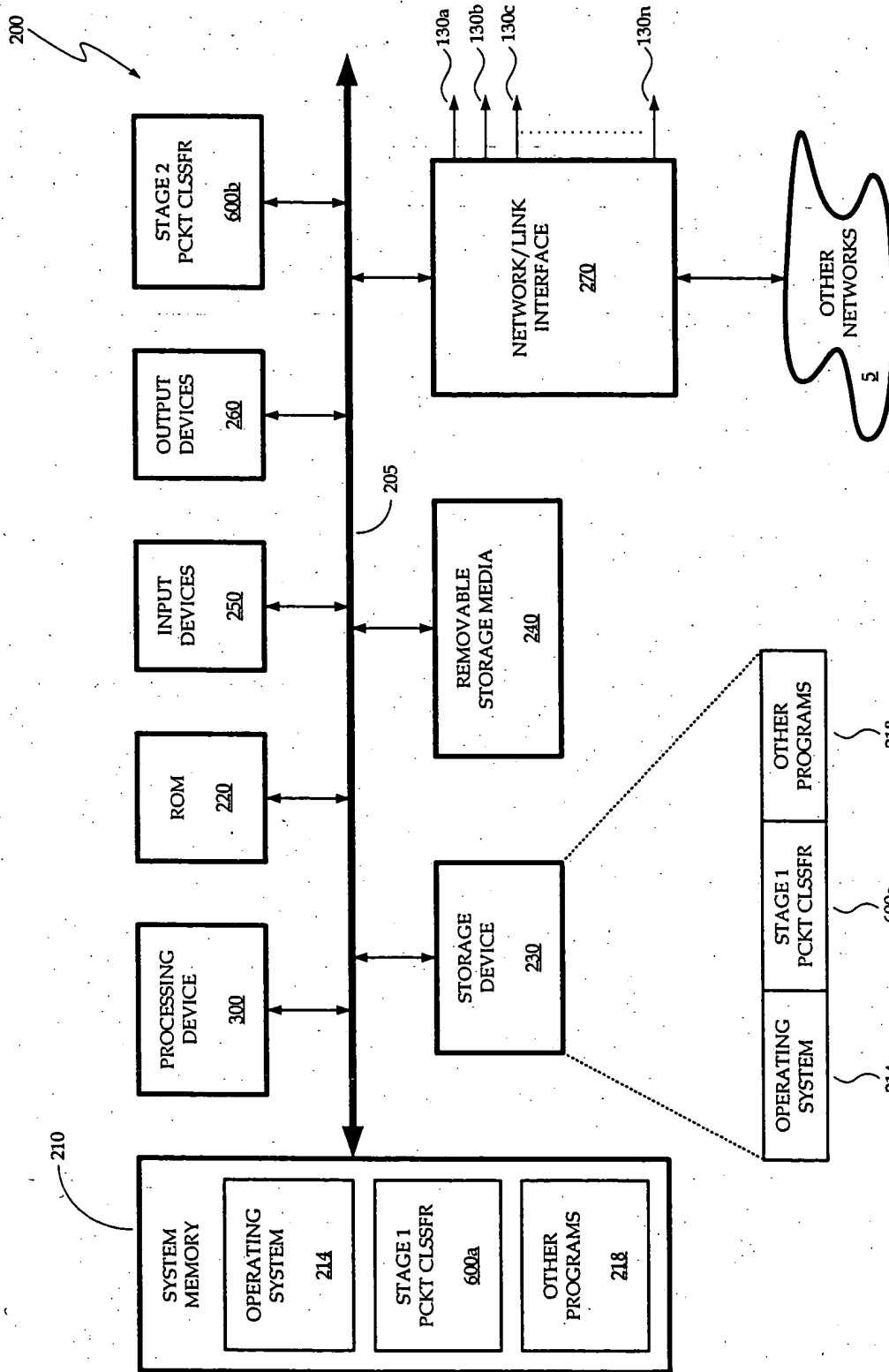


FIG. 2

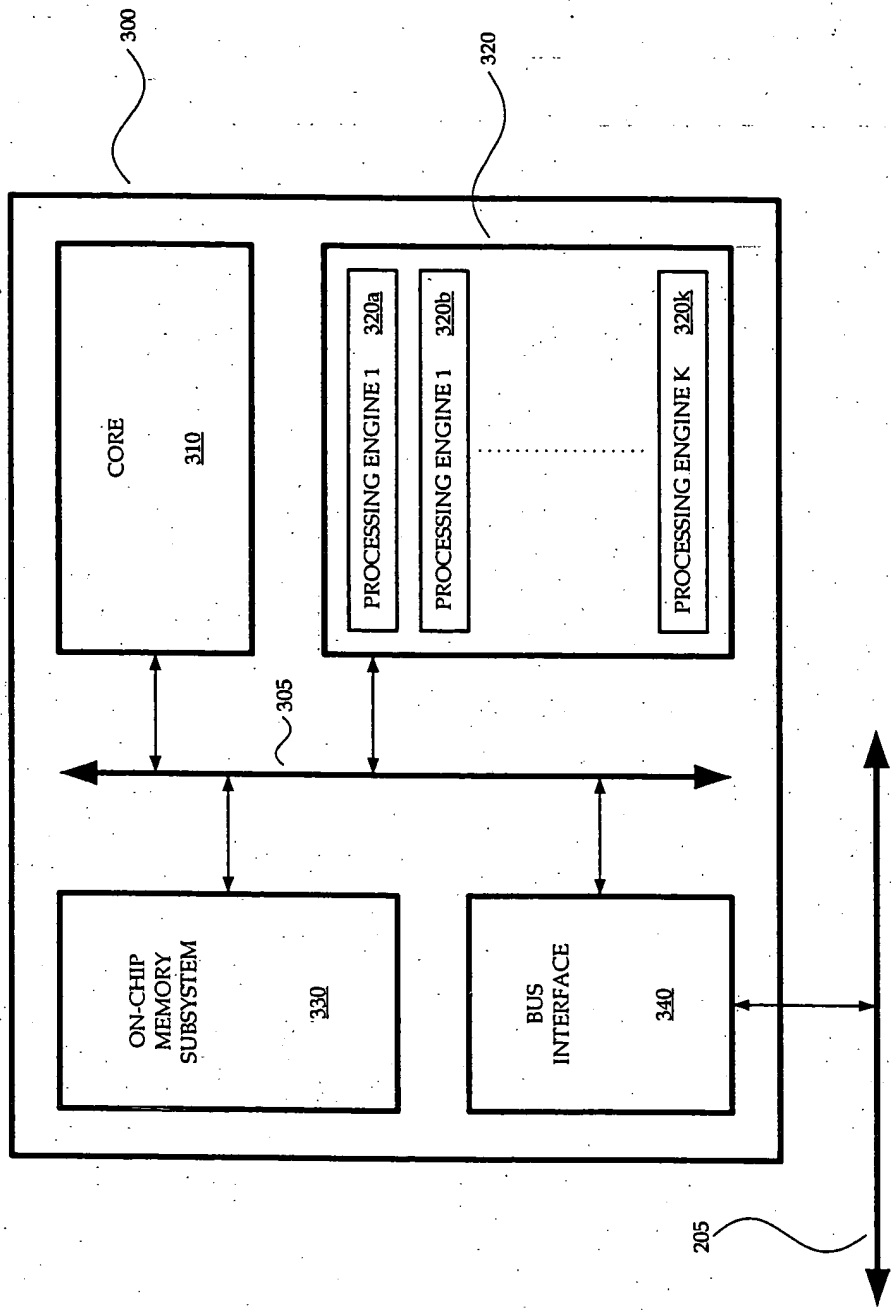


FIG. 3 (PRIOR ART)

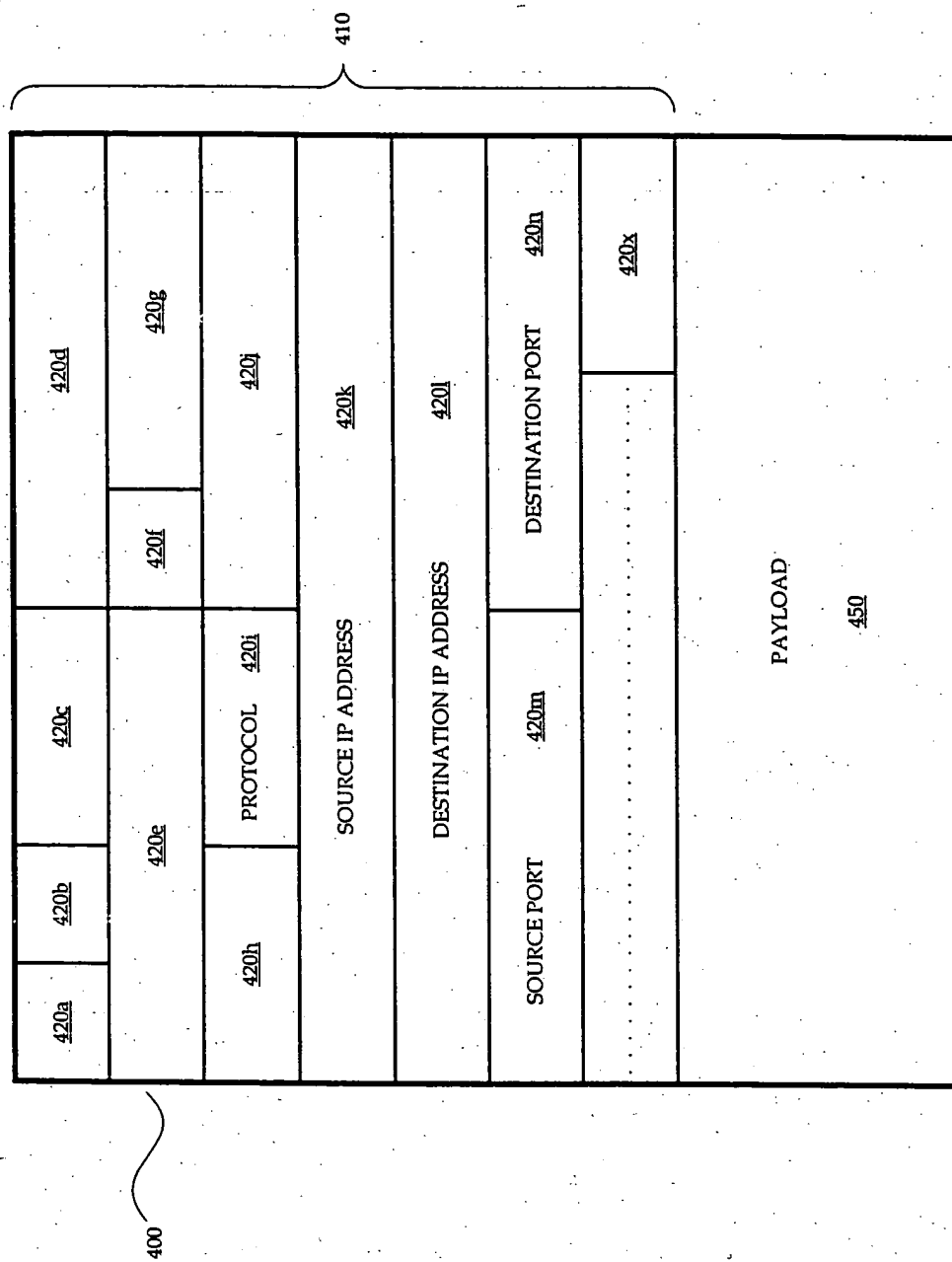


FIG. 4 (PRIOR ART)

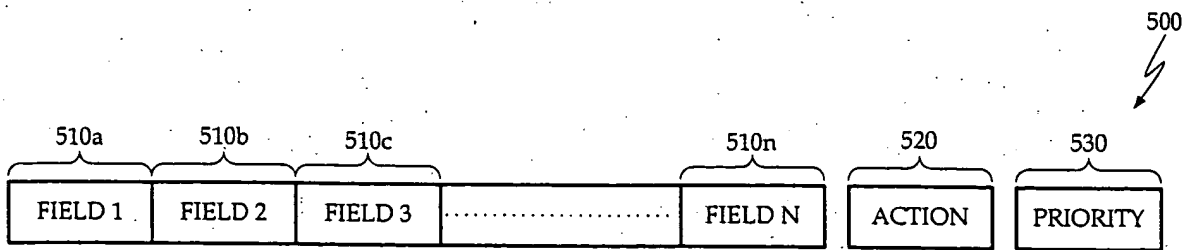


FIG. 5A (PRIOR ART)

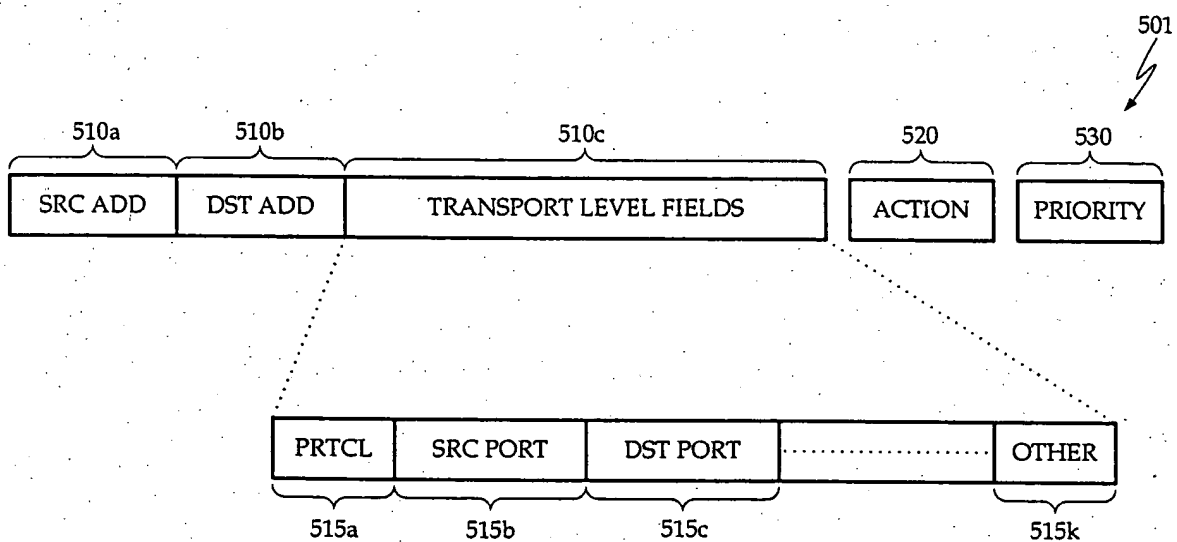


FIG. 5B (PRIOR ART)

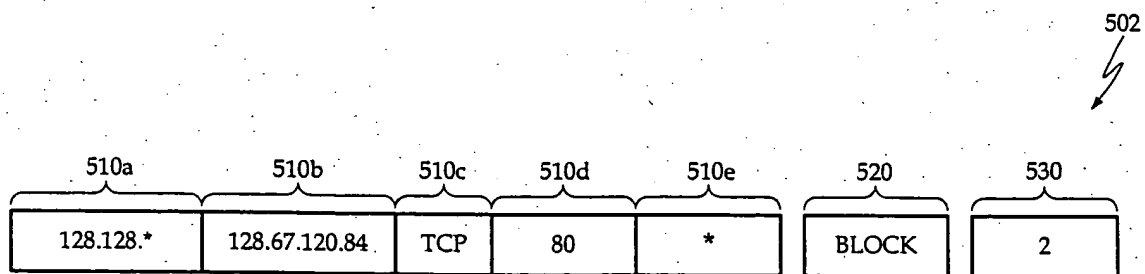


FIG. 5C (PRIOR ART)

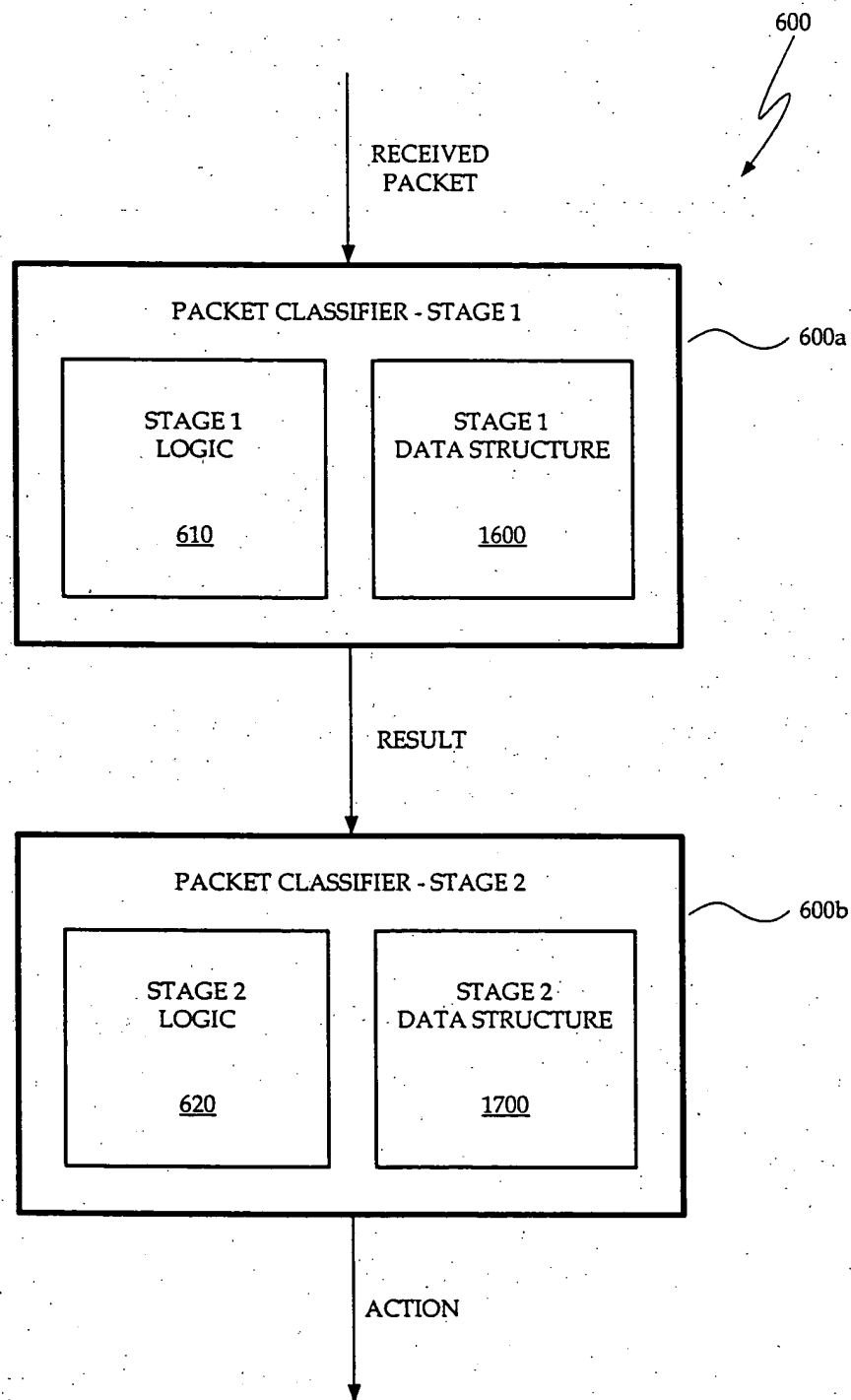


FIG. 6

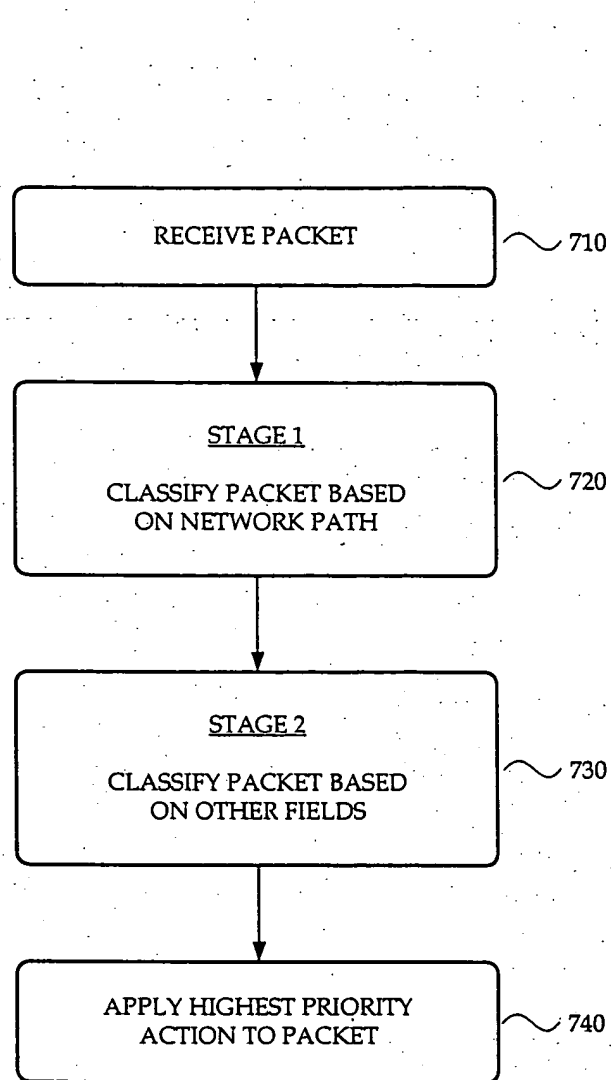



FIG. 7

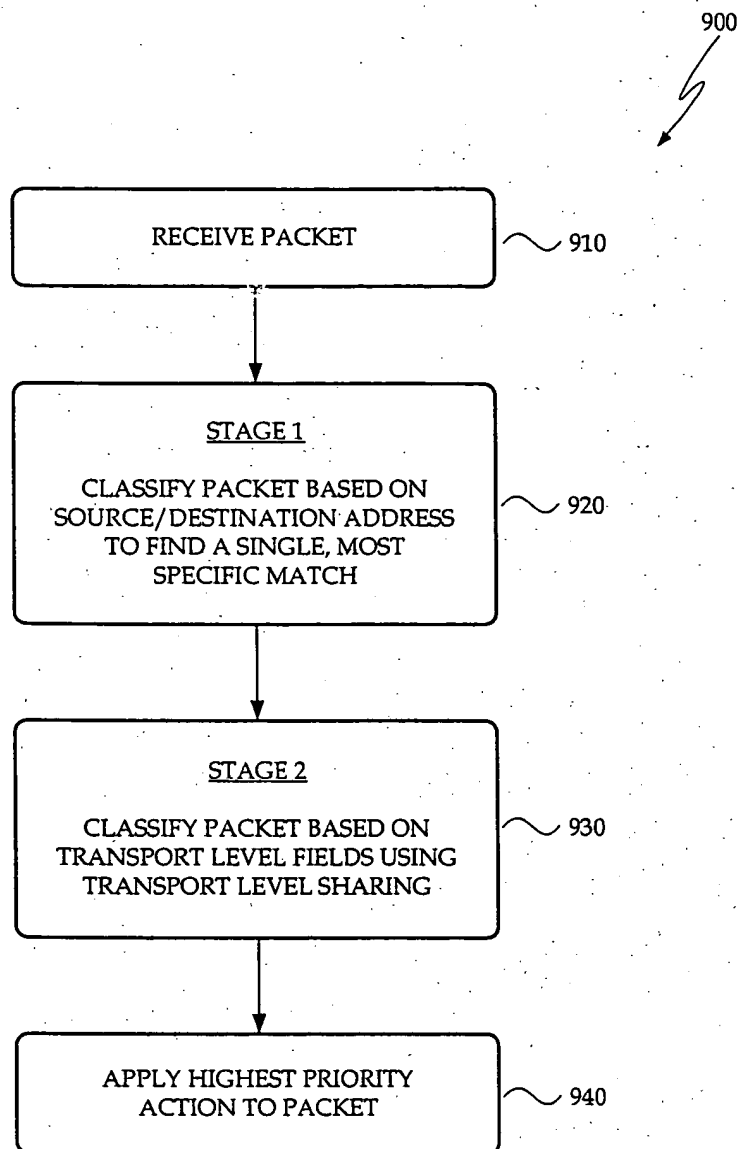
800



	810a	810b	810c	810d	810e	820	830a	830b
	SRC ADD	DST ADD	PRTCL	SRC PORT	DST PORT	ACTION	AB. PR.	REL. PR.
801	128.128.10.5	172.128.*	TCP	21	*	ACCEPT	12	1
	128.128.10.5	172.128.*	TCP	80	*	ACCEPT	13	2
	128.128.10.5	172.128.*	TCP	17	*	BLOCK	14	3
802	128.128.10.7	172.128.*	TCP	21	*	ACCEPT	15	1
	128.128.10.7	172.128.*	TCP	80	*	ACCEPT	16	2
	128.128.10.7	172.128.*	TCP	17	*	BLOCK	17	3

FIG. 8





*FIG. 9*

1000

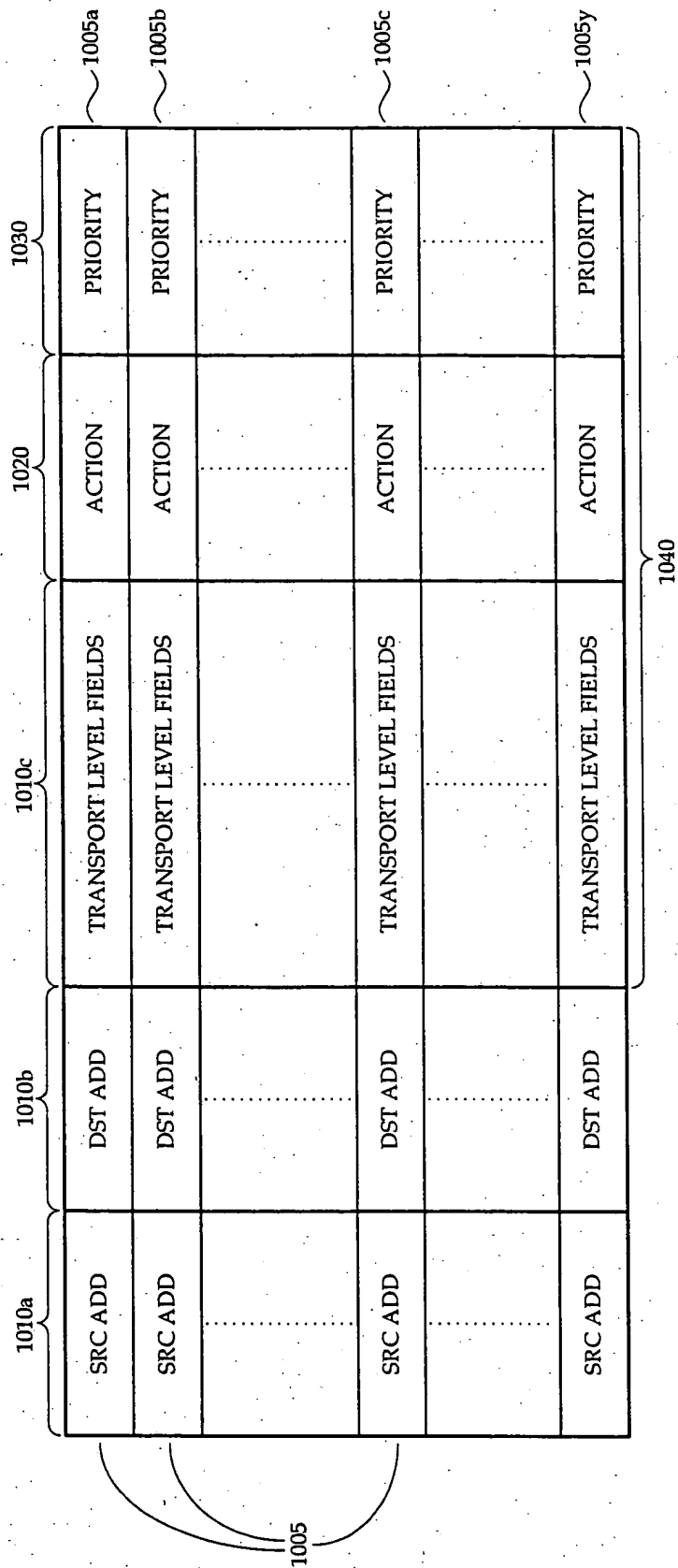


FIG. 10

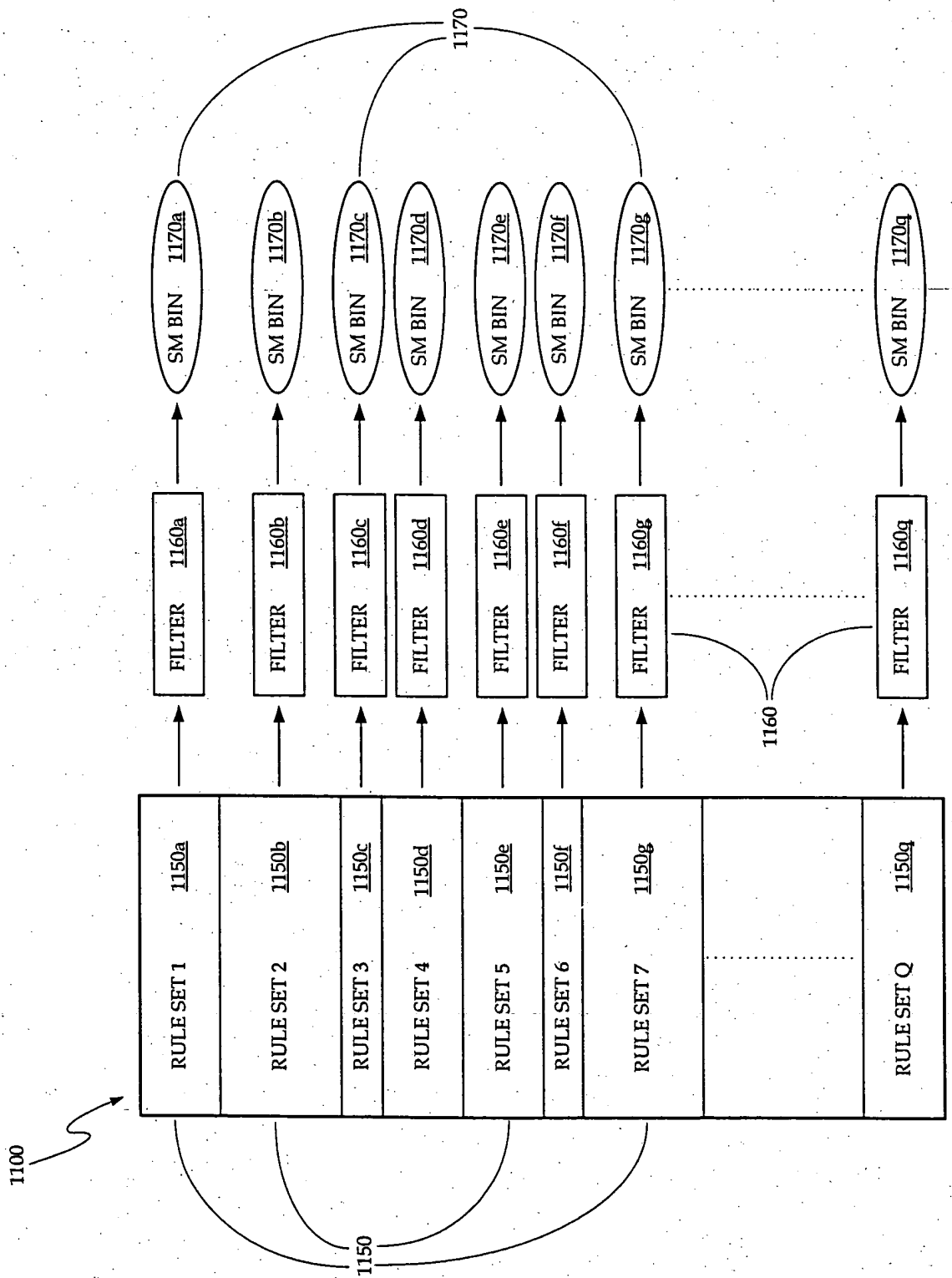


FIG. 11A

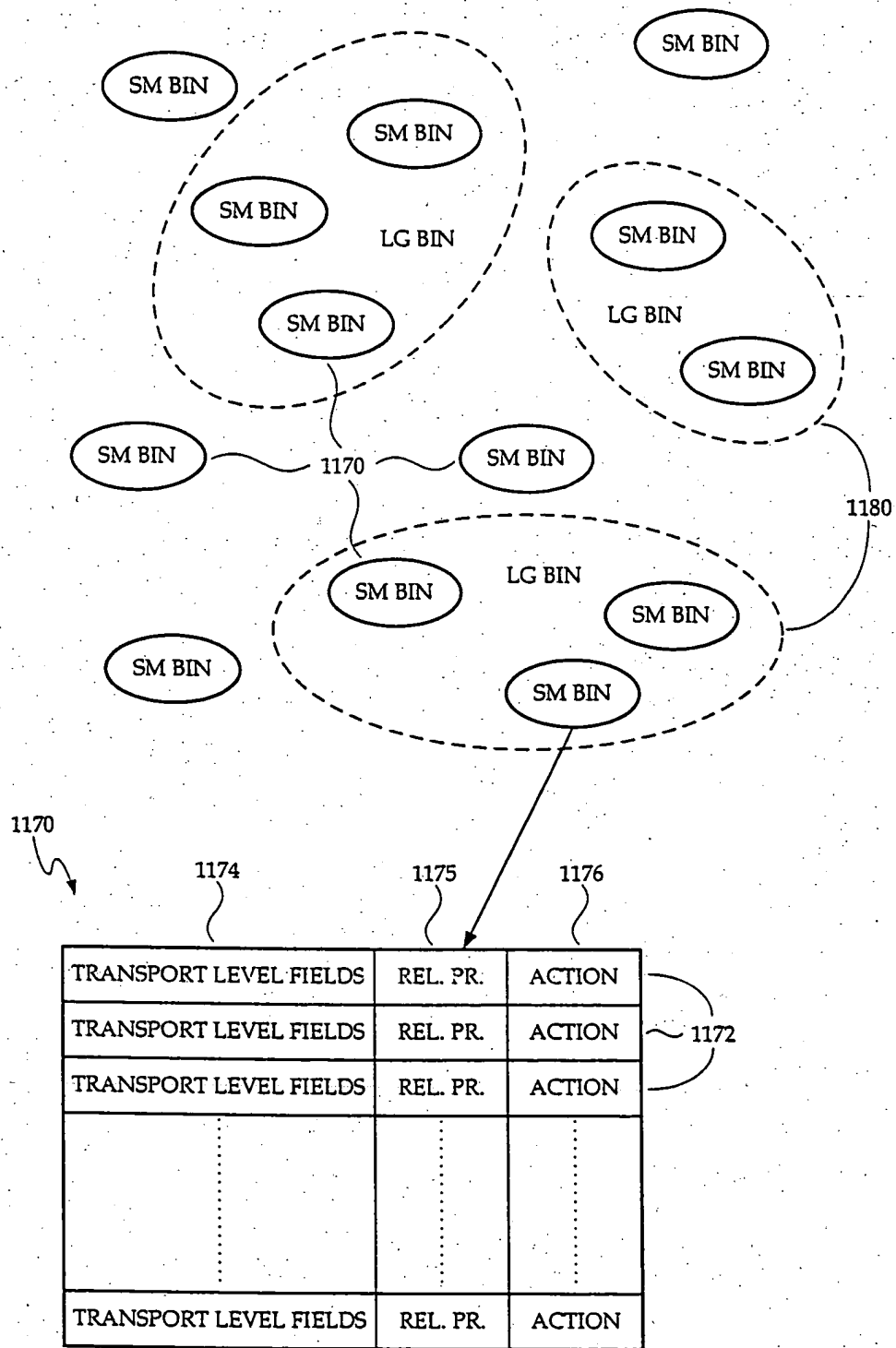
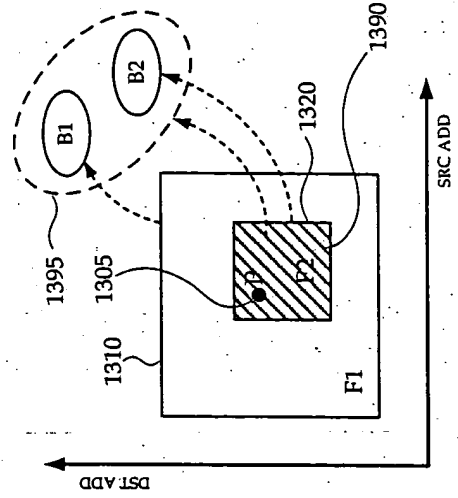
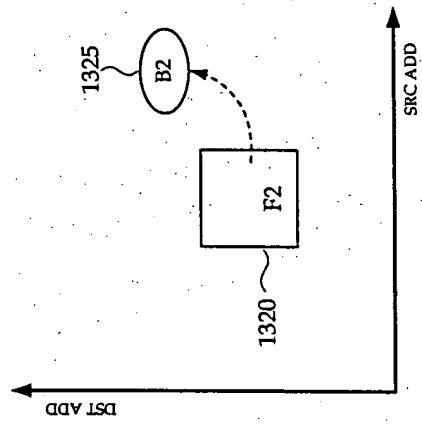
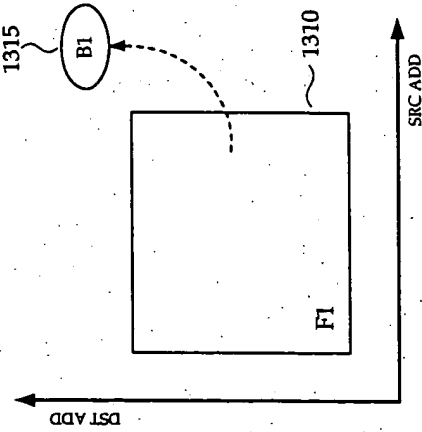
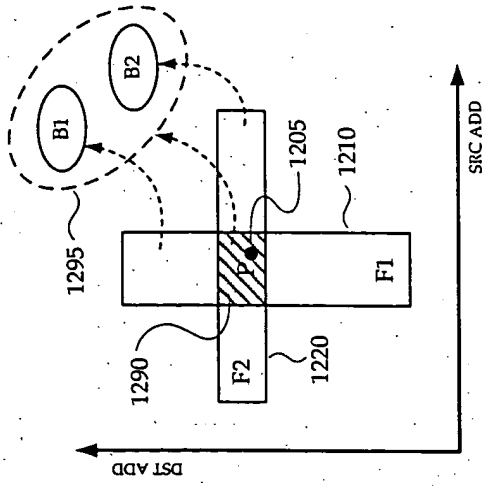
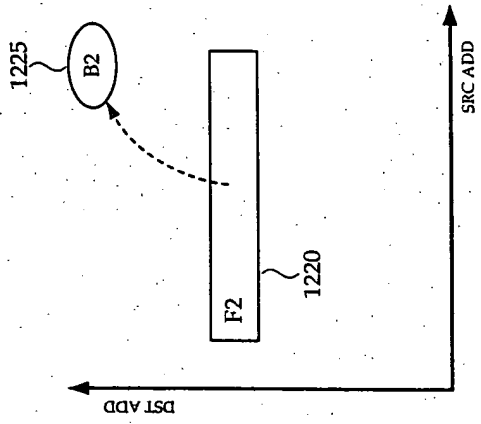
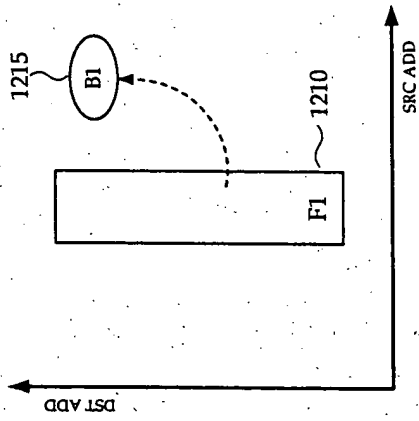
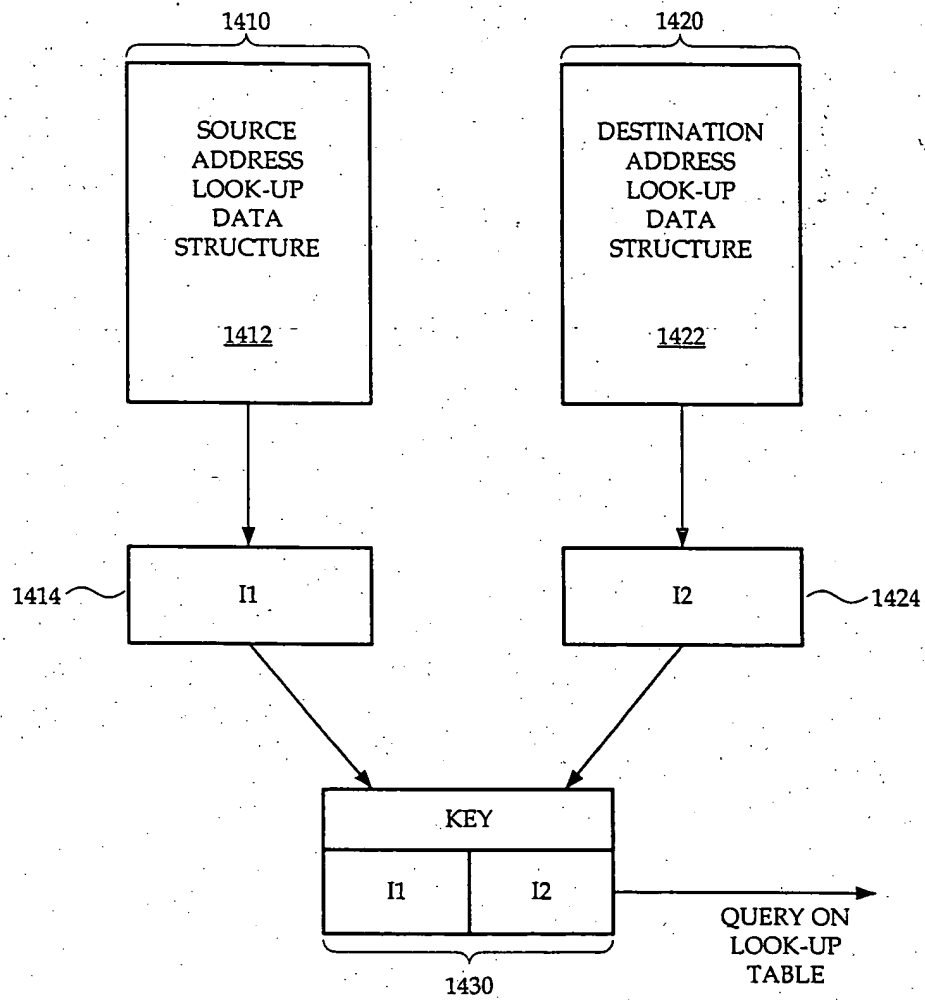


FIG. 11B





*FIG. 14*

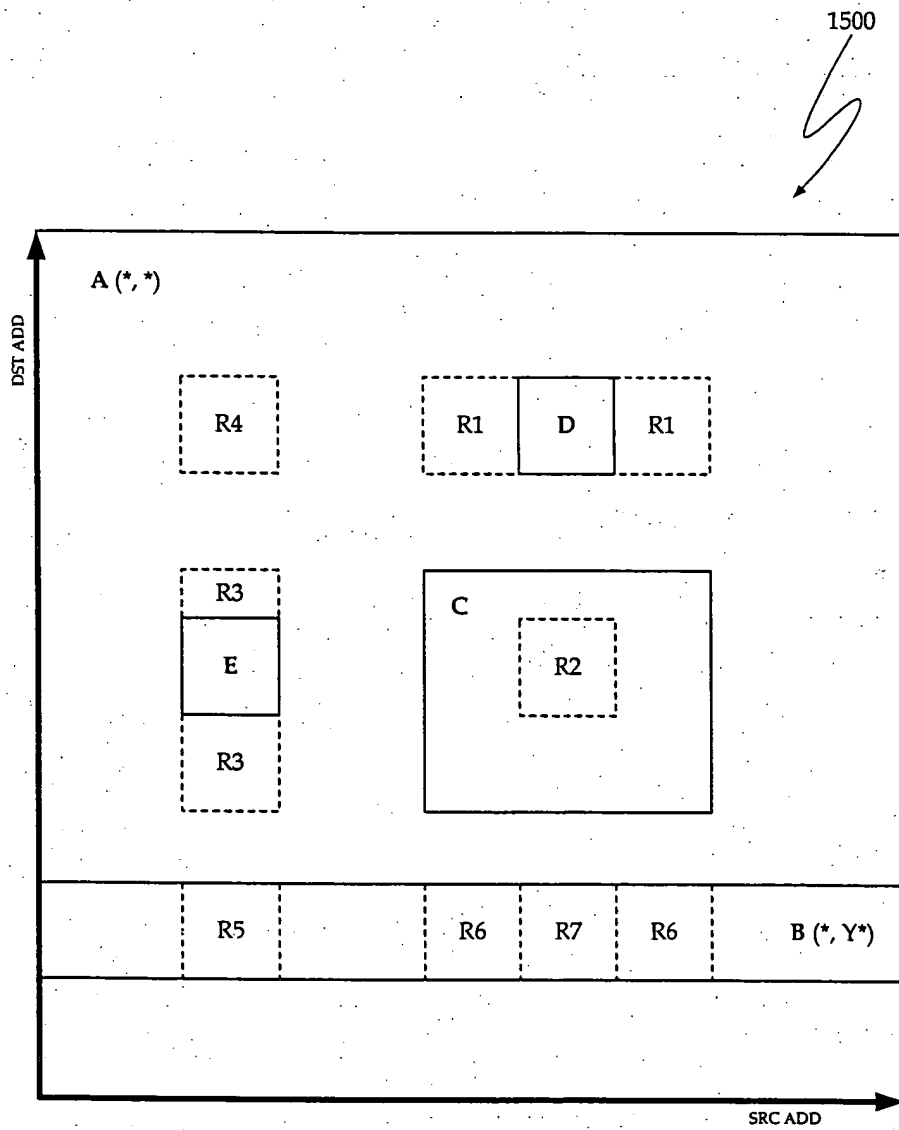


FIG. 15

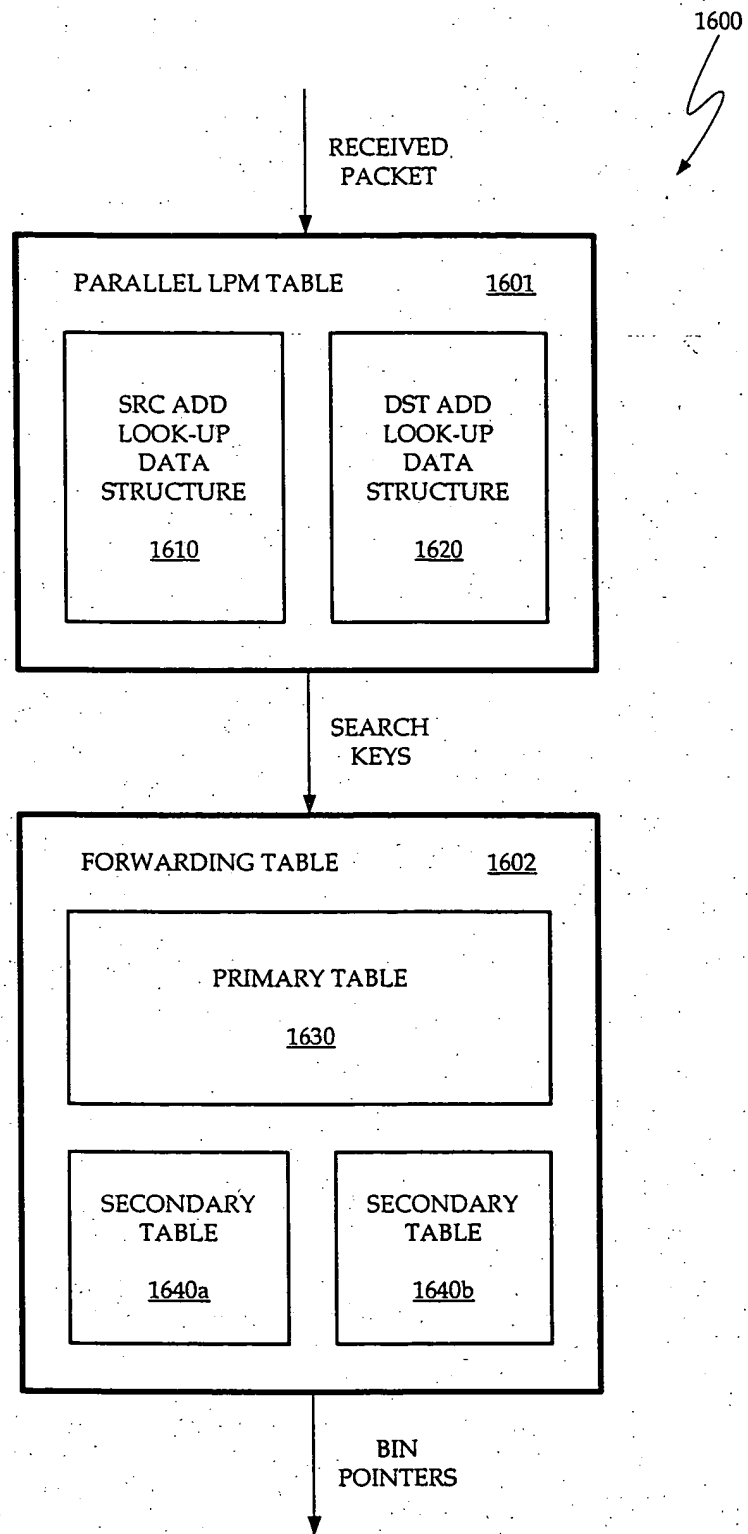


FIG. 16A



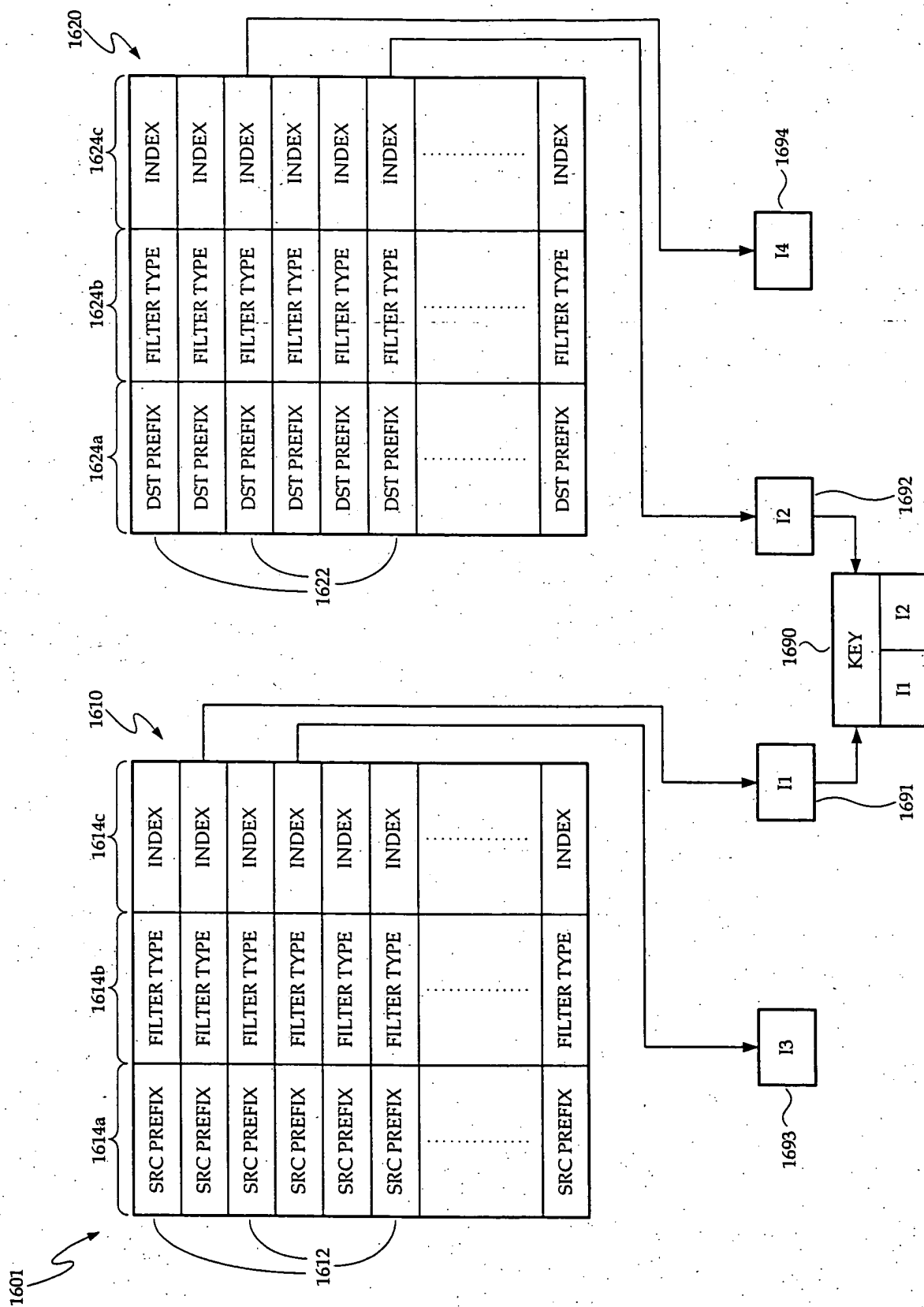


FIG. 16B

1630

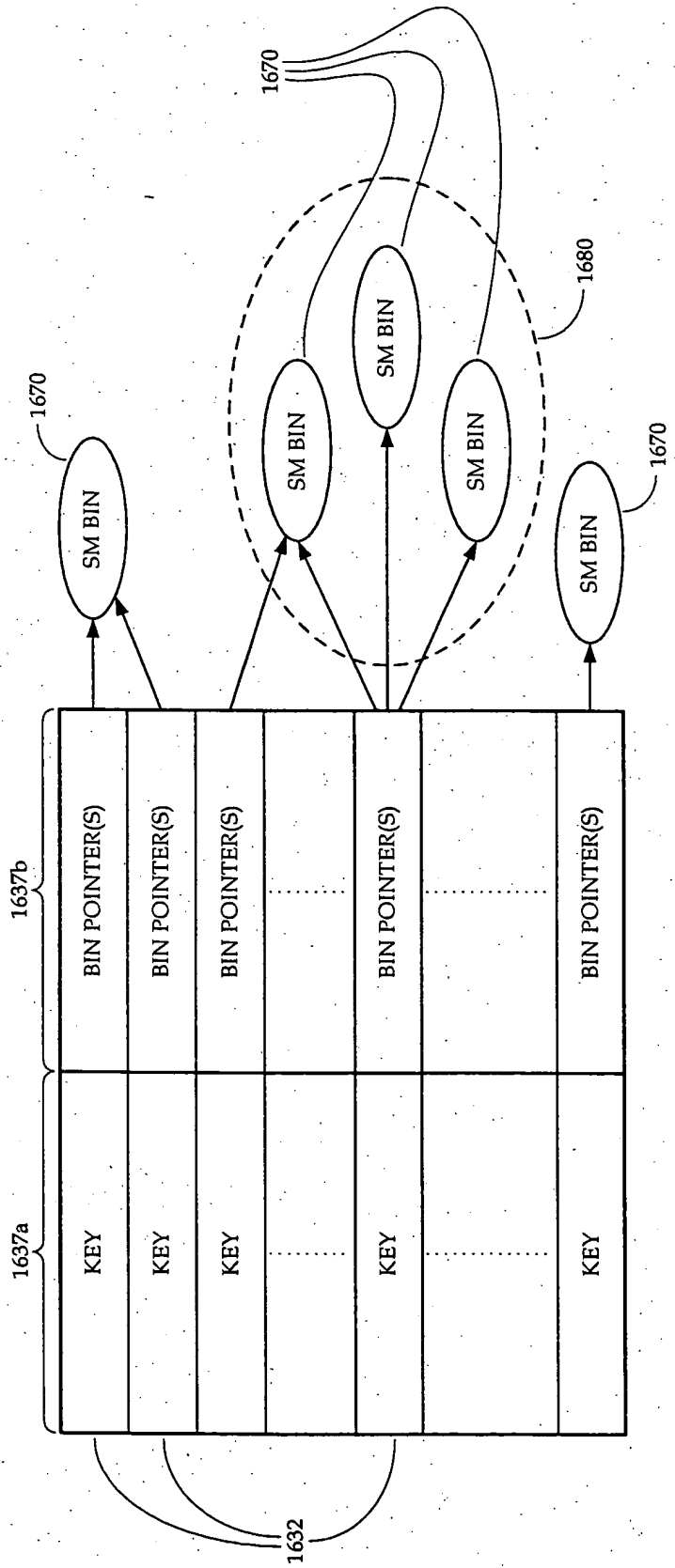


FIG. 16C

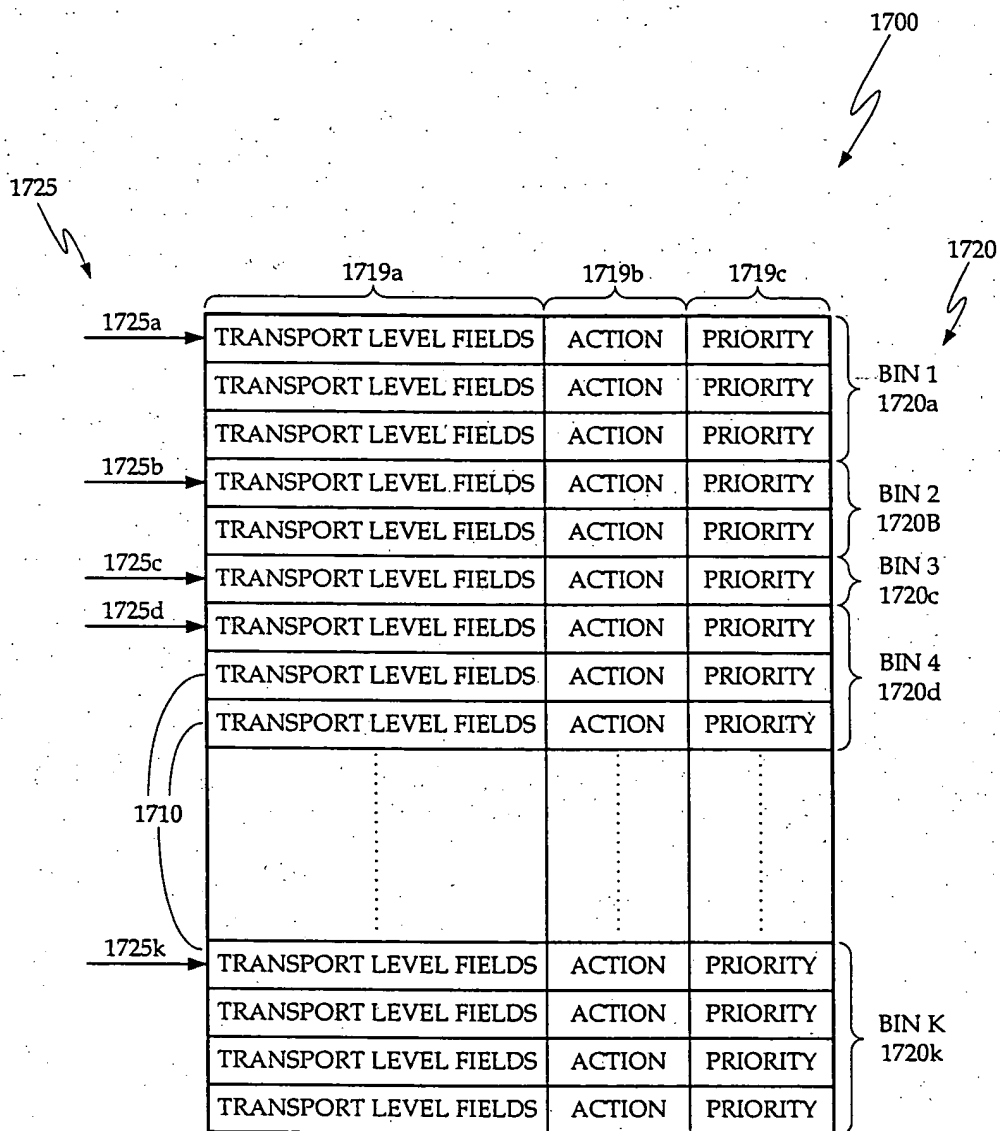


FIG. 17

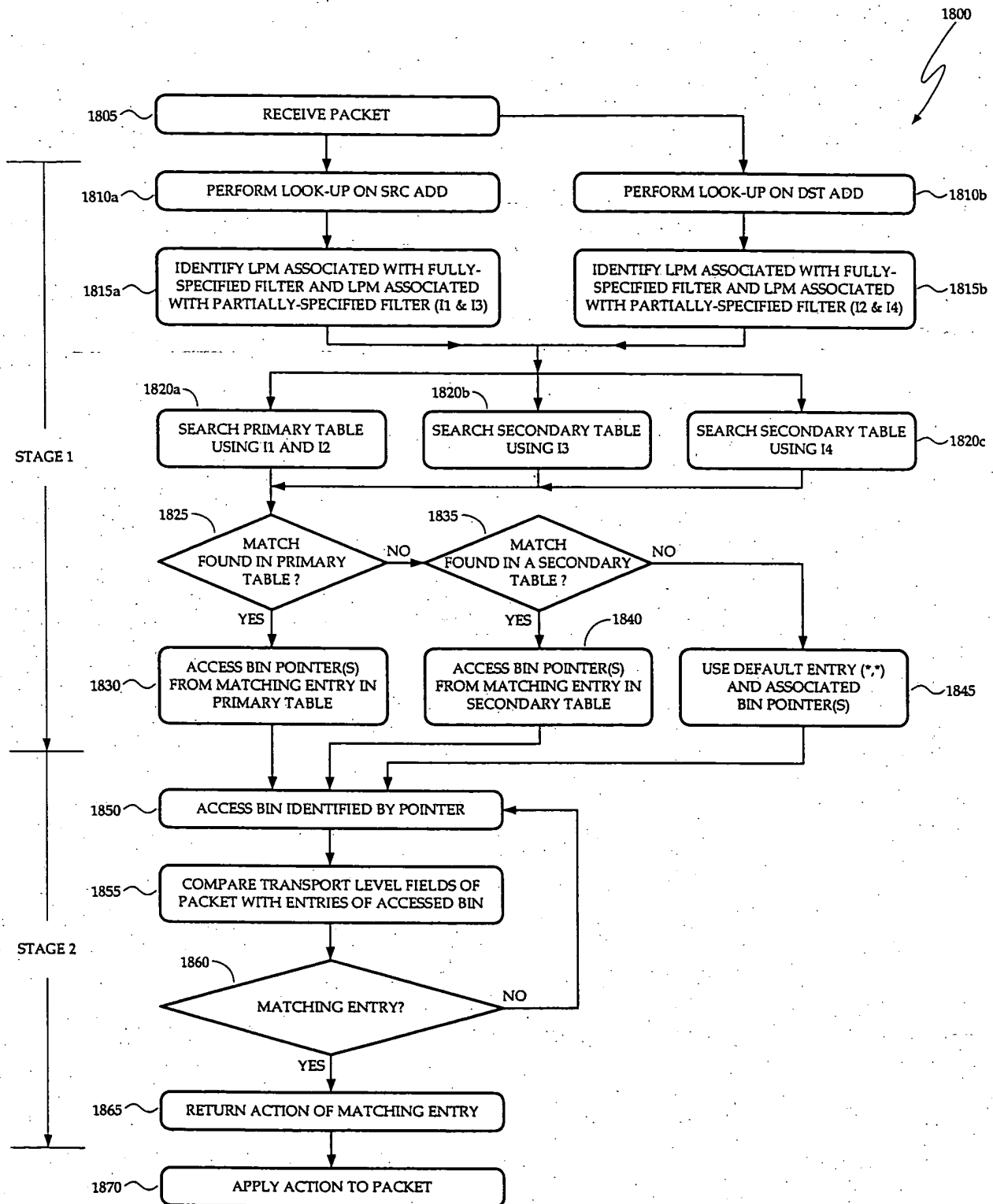


FIG. 18

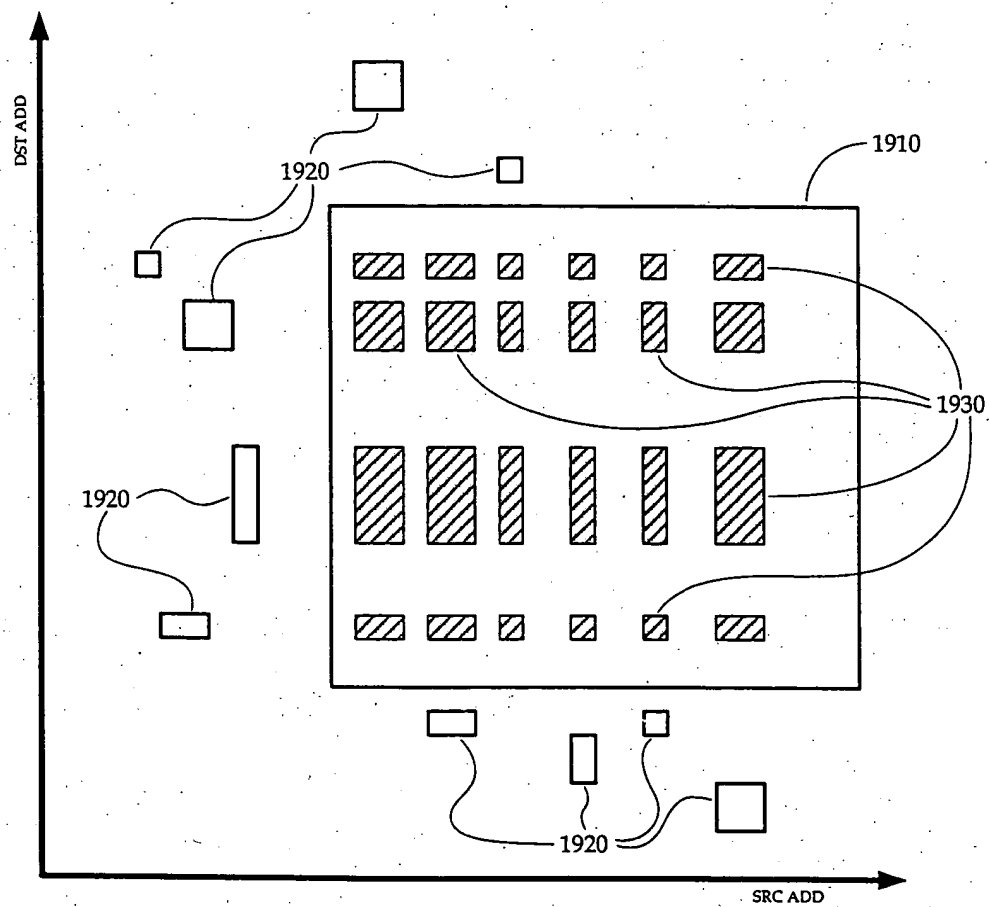


FIG. 19

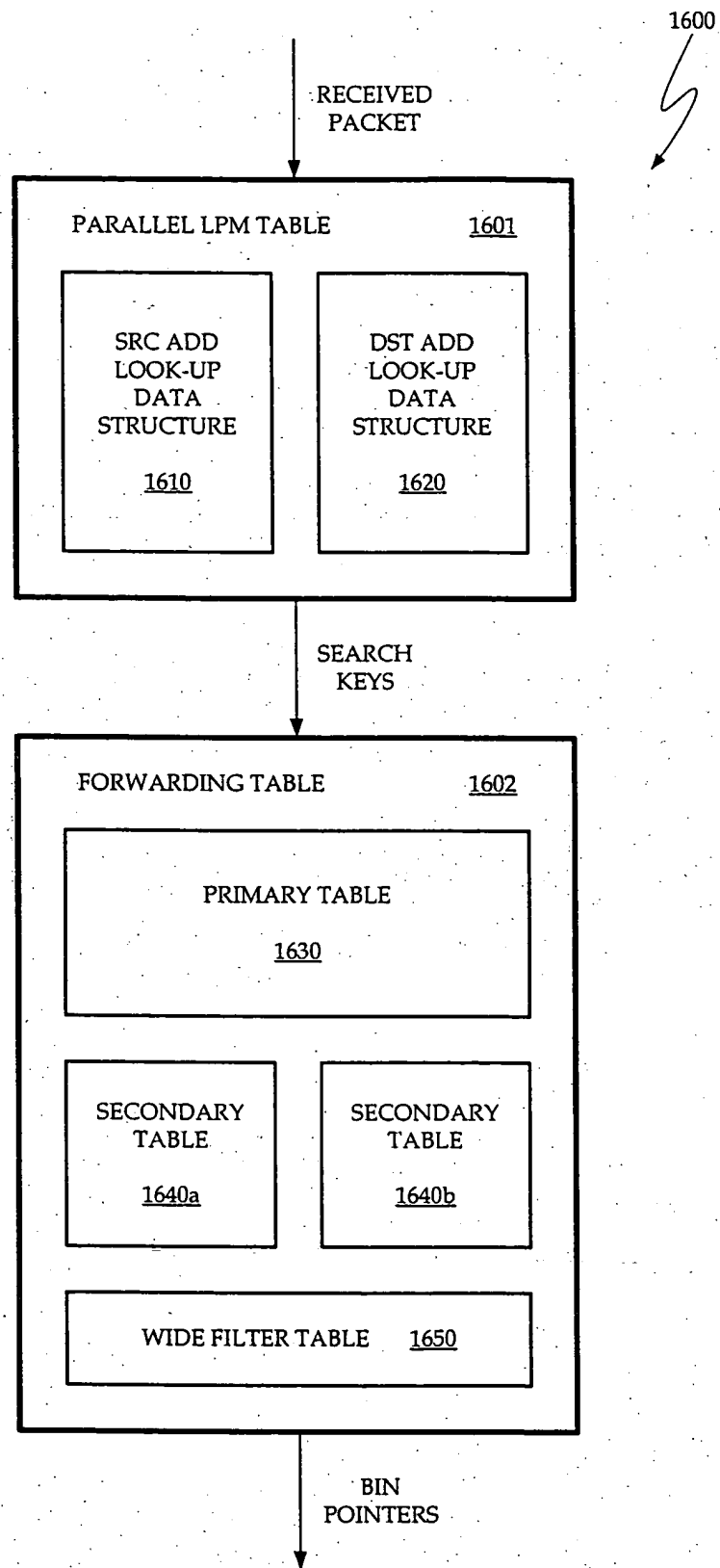
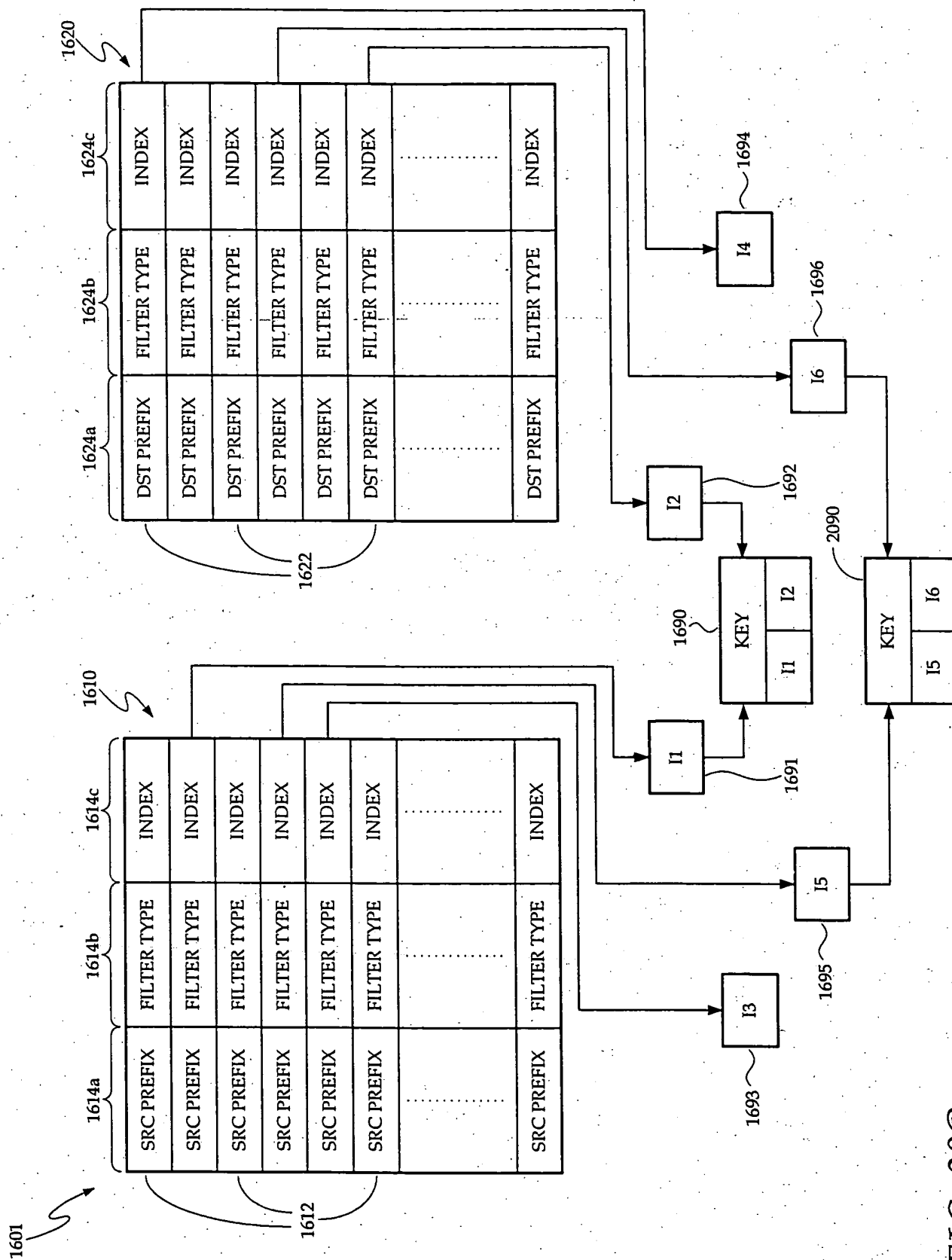


FIG. 20A



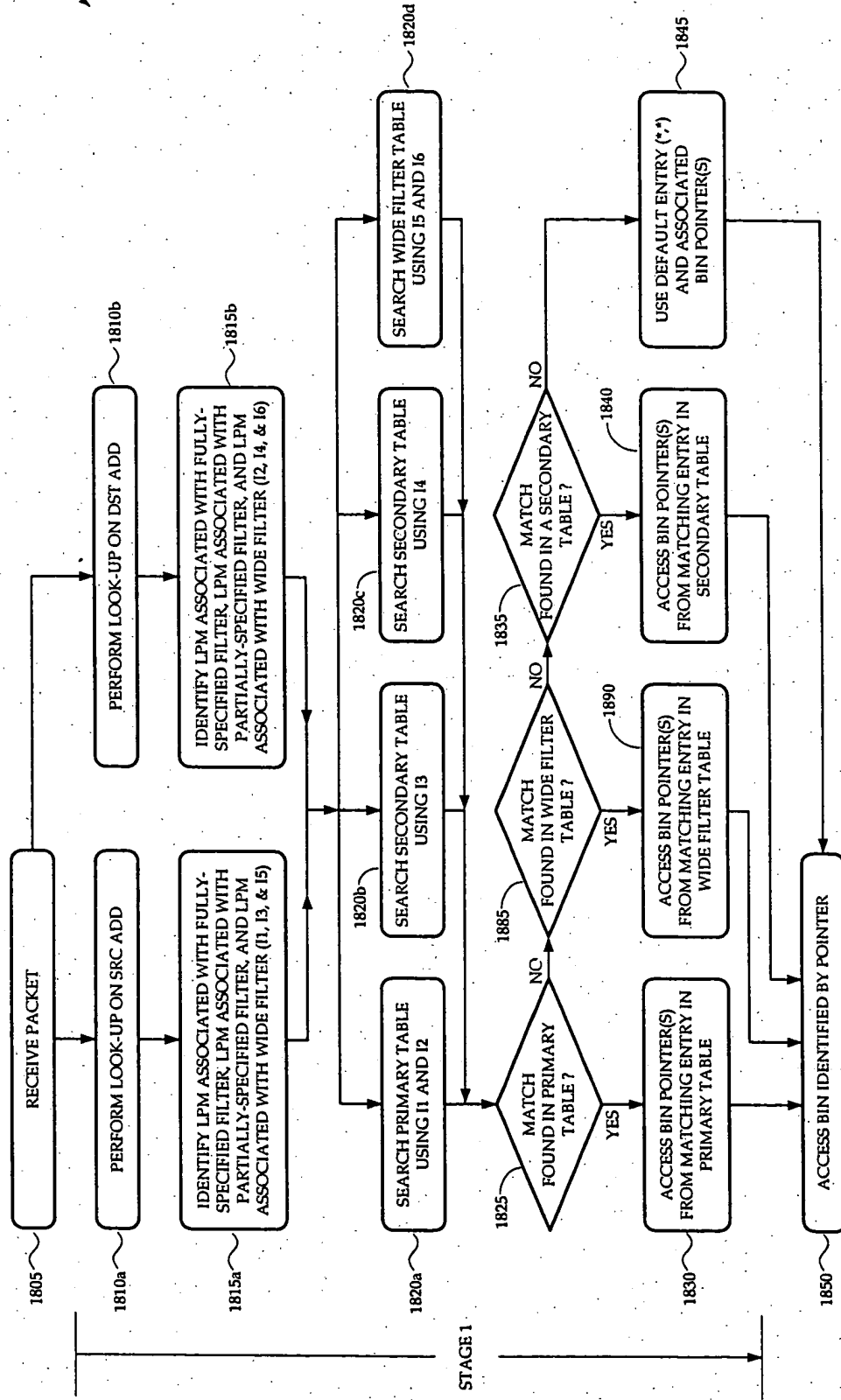


FIG. 20C

STAGE 2



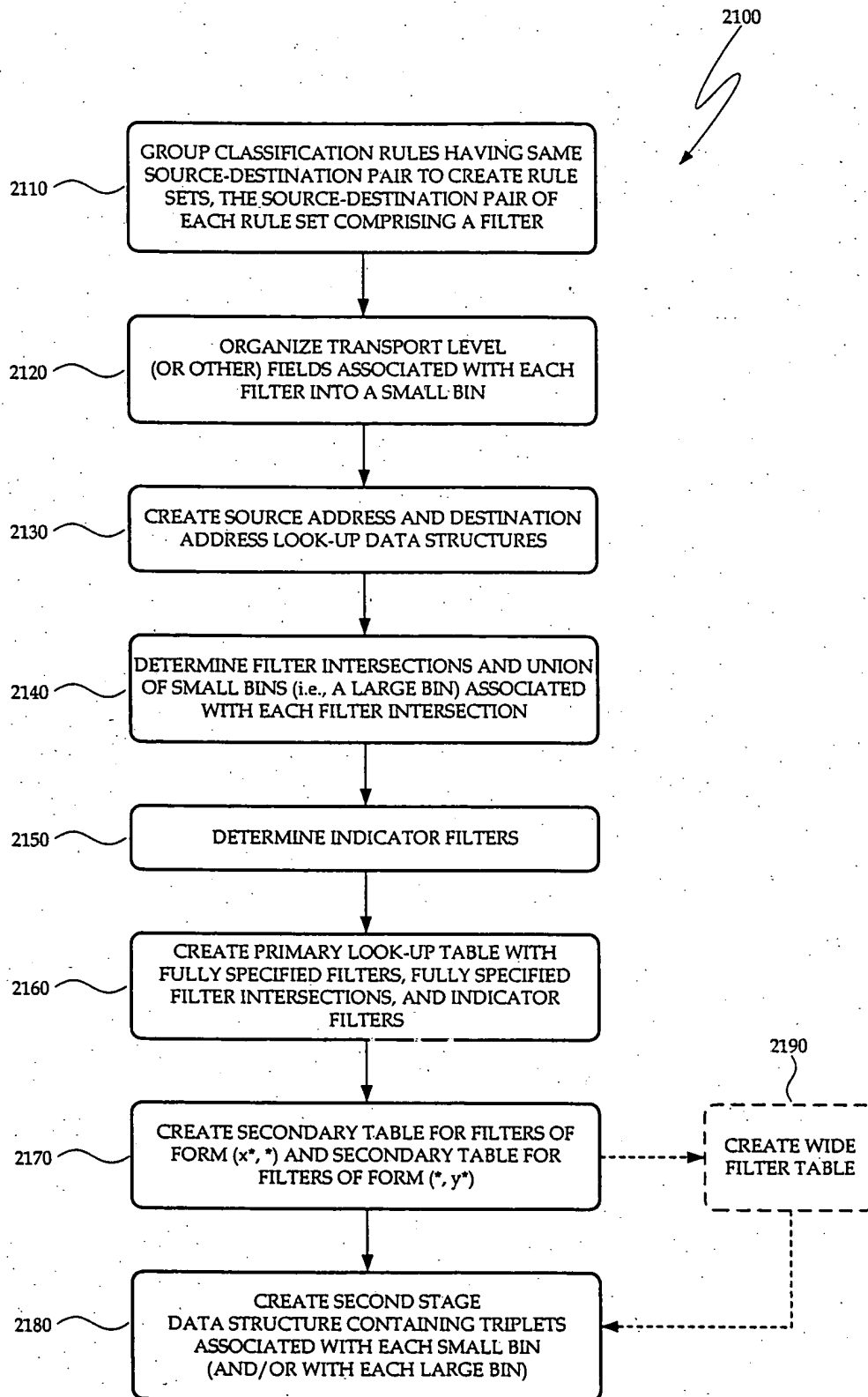


FIG. 21

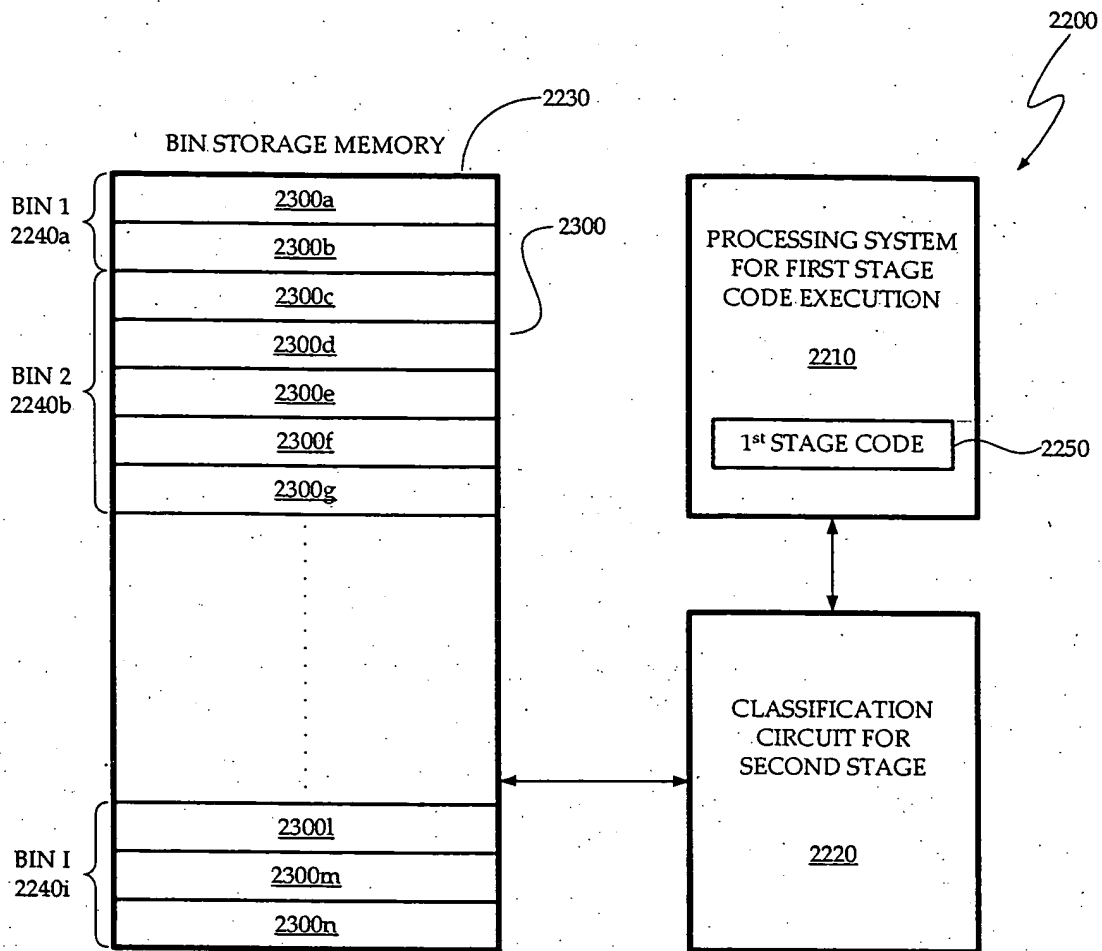


FIG. 22

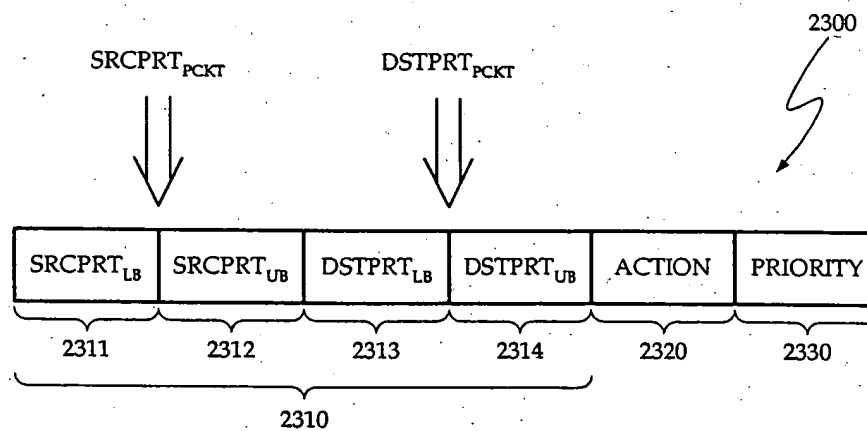


FIG. 23

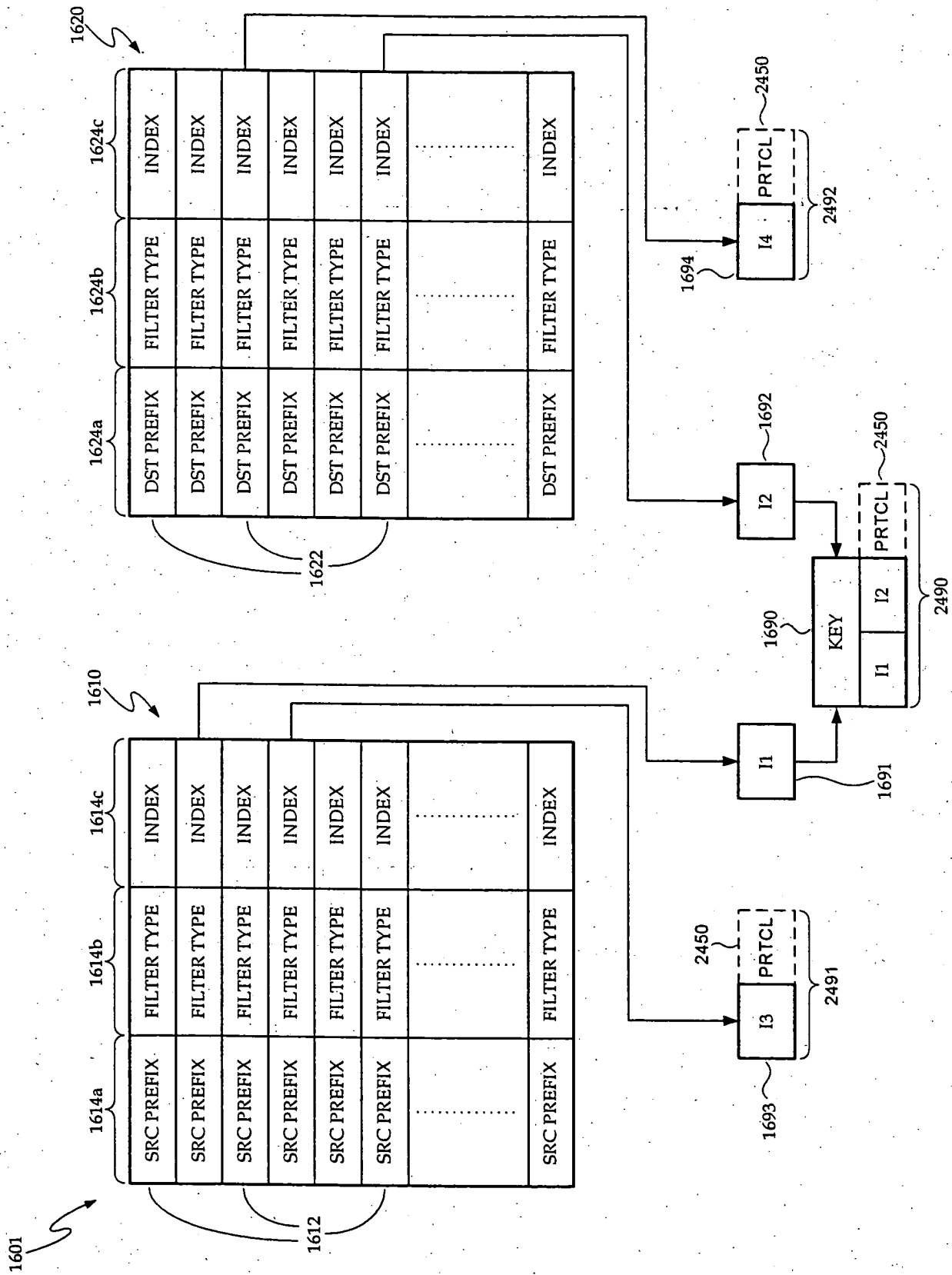


FIG. 24

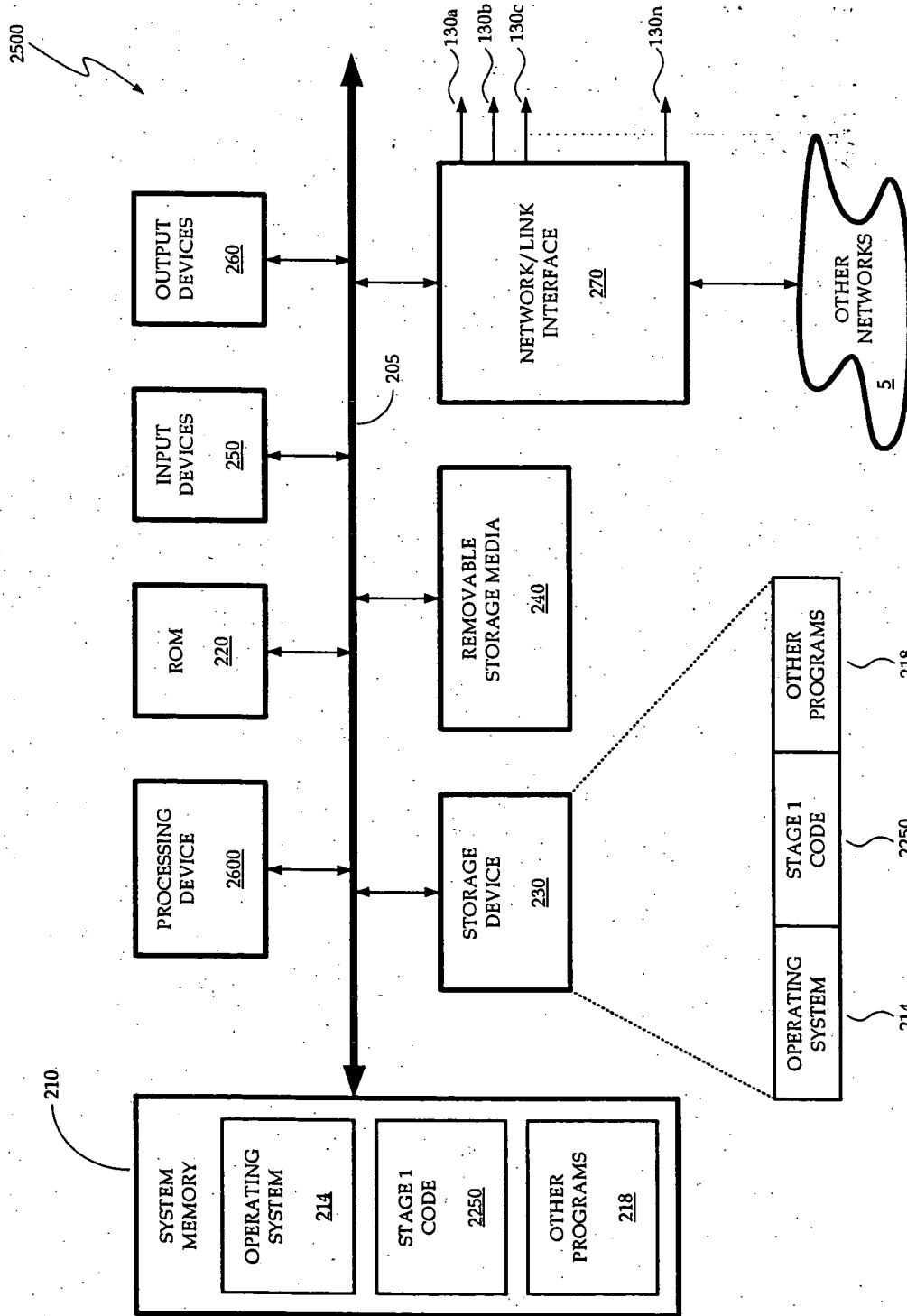


FIG. 25

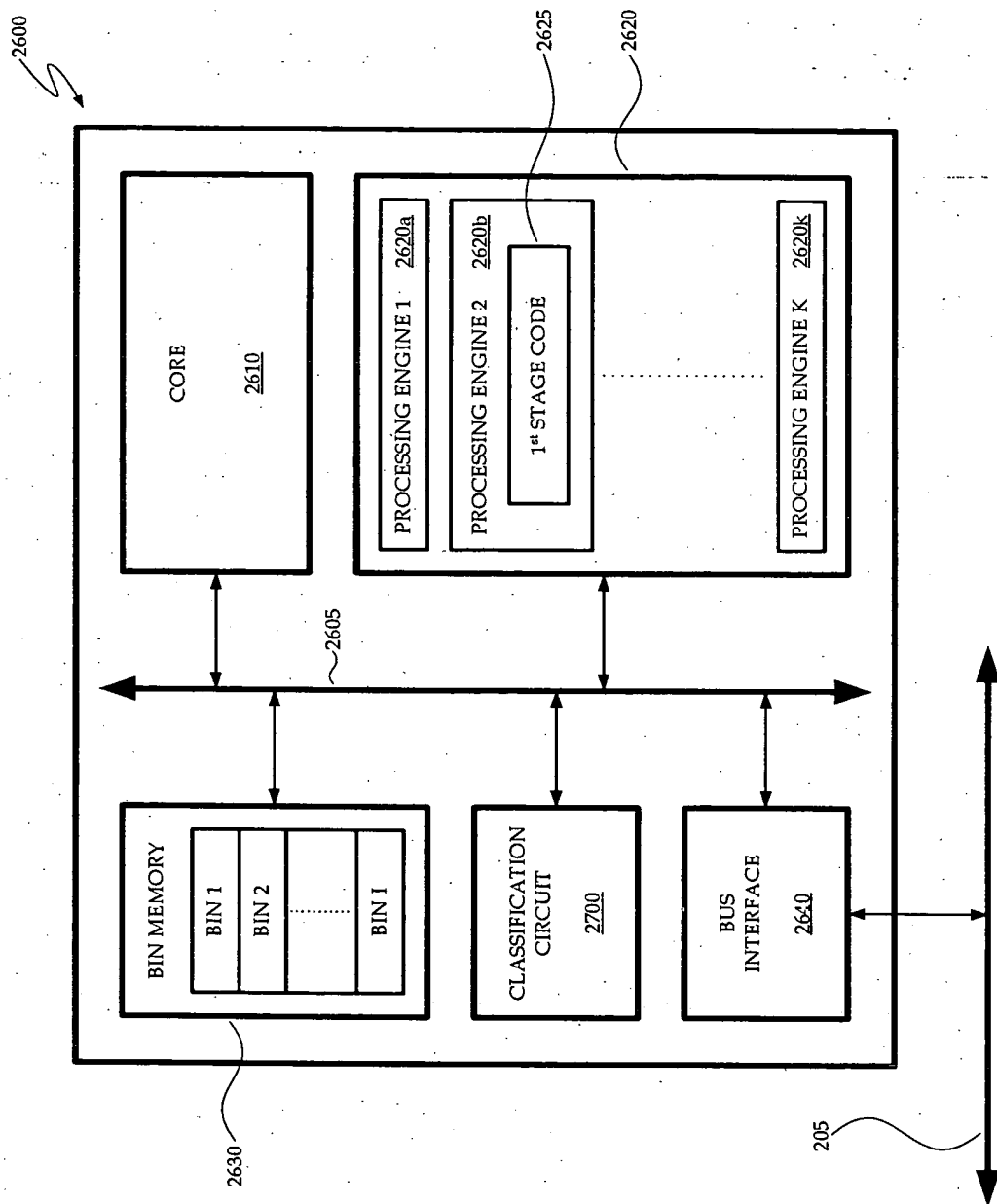


FIG. 26

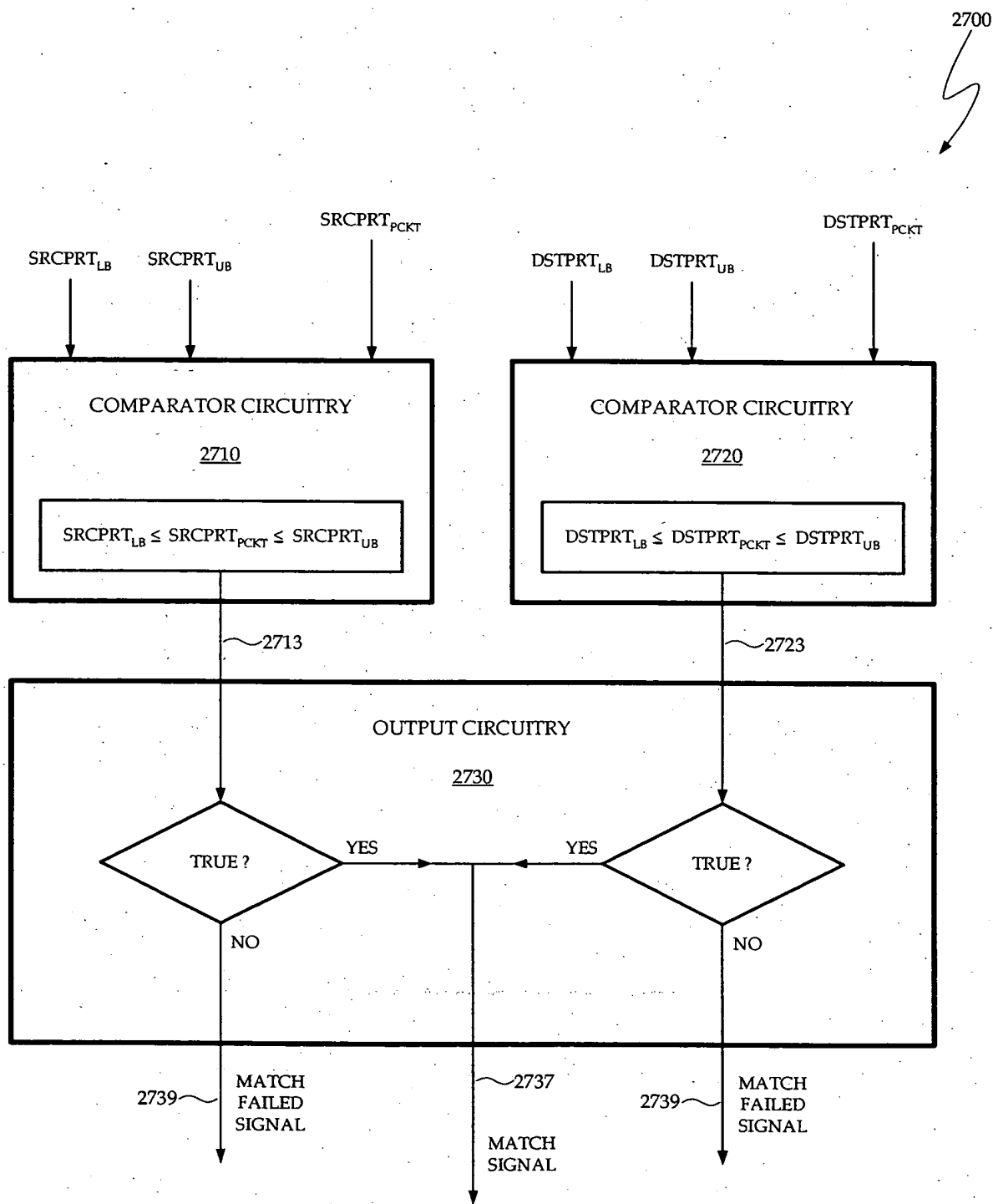


FIG. 27

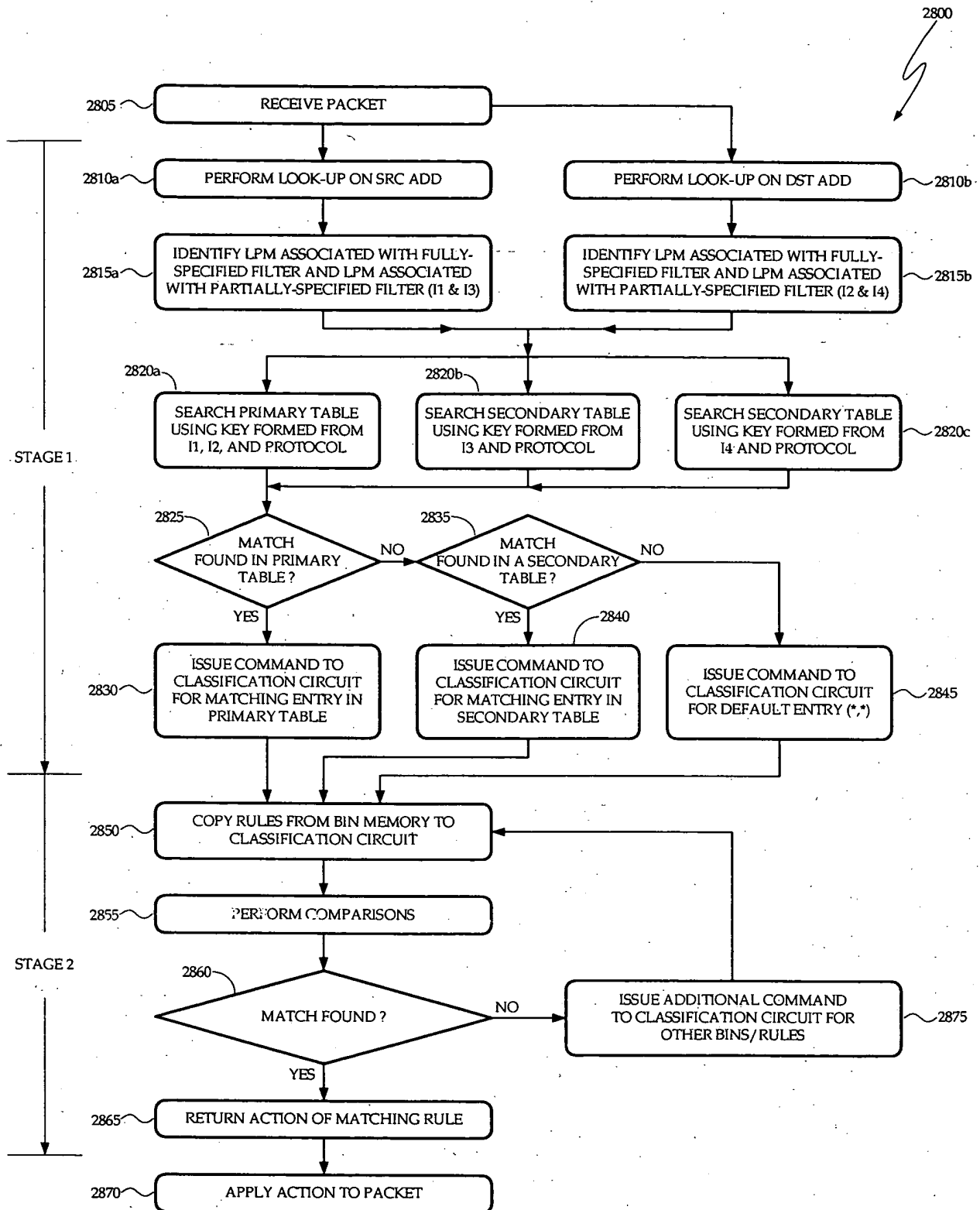


FIG. 28